



2124

JRW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named
Inventor : Sergej B. Gashkav et al.

Appln. No.: 10/017,802

Filed : December 12, 2001

For : OPTIMIZATION OF AN ADDER
BASED CIRCUIT ARCHITECTURE

Docket No.: L13.12-0173/00-630

Group Art Unit: 2124
Examiner: D.H.
Malzahn

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I HEREBY CERTFY THAT THIS PAPER IS BEING
SENT BY U.S. MAIL, FIRST CLASS, TO THE
COMMISSIONER FOR PATENTS, P.O. BOX 1450,
ALEXANDRIA, VA 22313-1450 THIS

25 DAY OF October, 2004.
D - D - B
PATENT ATTORNEY

Sir:

This amendment is in response to the Official Action
dated October 6, 2004. Please amend the above-identified
application as follows: